

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **YOKOTA, et al.**

**RECEIVED**

Serial No.: **10/067,881** ✓

**FEB 20 2004**

Group Art Unit: **8327** <sup>2825</sup>

Filed: **February 8, 2002**

Examiner: **DO, THUAN V.**

For: **SEMICONDUCTOR DEVICE HAVING EMBEDDED ARRAY**

P.T.O. Confirmation No.: **8327**

**REQUEST FOR STATUS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: February 17, 2004

Sir:

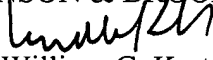
The undersigned attorney requests the status of the above-identified application.

The application was filed on **February 8, 2002** in the U.S. Patent and Trademark Office  
and to date no Office Action has been received.

In the event any fees are required in connection with this paper, please charge Deposit  
Account No. 01-2340.

Respectfully submitted,

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**23850**

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